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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.	
10/803,756	03/18/2004	Ryo Yamazaki	16004.1020	6892	
35856	35856 7590 06/28/2005			EXAMINER	
SMITH FRO	OHWEIN TEMPEL G	NGUYEN, KHANH V			
	P.O. BOX 88148 ATLANTA, GA 30356		ART UNIT	PAPER NUMBER	
			2817		
			DATE MAILED: 06/28/200	5	

Please find below and/or attached an Office communication concerning this application or proceeding.

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	÷.	Application No.	Applicant(s)			
Office Action Summers		10/803,756	YAMAZAKI ET AL.			
	Office Action Summary	Examiner	Art Unit			
	TI MAN NO DATE AND A	Khanh V. Nguyen	2817			
Period f	The MAILING DATE of this communication or Reply	appears on the cover sheet w	ith the correspondence address			
THE - Extended - If th - If No - Failth - Any	MAILING DATE OF THIS COMMUNICATION OF THIS C	ON. FR 1.136(a). In no event, however, may a n. a reply within the statutory minimum of thir eriod will apply and will expire SIX (6) MOP statute, cause the application to become A	reply be timely filed ty (30) days will be considered timely. NTHS from the mailing date of this communication. BANDONED (35 U.S.C. § 133).			
Status						
1)🛛	Responsive to communication(s) filed on 3	<u>3/18/04</u> .				
2a) <u></u>		nis action is non-final.				
3)[	Since this application is in condition for allowance except for formal matters, prosecution as to the merits is					
	closed in accordance with the practice under Ex parte Quayle, 1935 C.D. 11, 453 O.G. 213.					
Disposit	ion of Claims	·				
5)□ 6)⊠ 7)⊠	Claim(s) 1-18 is/are pending in the applica 4a) Of the above claim(s) is/are with Claim(s) is/are allowed. Claim(s) 1-4,7,8,10-12 and 16-18 is/are re Claim(s) 5, 6, 9, 13-15 is/are objected to. Claim(s) are subject to restriction and	ndrawn from consideration.				
Applicat	ion Papers					
9)🛛	The specification is objected to by the Exar	miner.				
10)[	10) The drawing(s) filed on is/are: a) □ accepted or b) □ objected to by the Examiner.					
	Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).					
11)□	Replacement drawing sheet(s) including the co The oath or declaration is objected to by the	· · ·	• • • • • • • • • • • • • • • • • • • •			
	under 35 U.S.C. § 119					
12)[ a)	Acknowledgment is made of a claim for formula All b) Some * c) None of:  1. Certified copies of the priority documents.  2. Certified copies of the priority documents.  3. Copies of the certified copies of the application from the International Bussee the attached detailed Office action for a	nents have been received. nents have been received in A priority documents have been ireau (PCT Rule 17.2(a)).	Application No  received in this National Stage			
Attachmer	nt(s)					
1) Moti	ce of References Cited (PTO-892)	4) Interview	Summary (PTO-413)			
3) 🔲 Info	ce of Draftsperson's Patent Drawing Review (PTO-948 mation Disclosure Statement(s) (PTO-1449 or PTO/Ster No(s)/Mail Date	Paper No(	s)/Mail Date Informal Patent Application (PTO-152)			
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#### **DETAILED ACTION**

### Specification

The disclosure is objected to because of the following informalities: Application Number of the related application is needed. Appropriate correction is required.

#### Claim Objections

Claims 16, 18 are objected to because of the following informalities:

Claim 16, page 14, line 4, "an second" should correctly be --a second--.

Claim 18, line 2, "a integrator" should correctly be --an integrator--. Appropriate correction is required.

#### Claim Rejections - 35 USC § 112

The following is a quotation of the second paragraph of 35 U.S.C. 112:

The specification shall conclude with one or more claims particularly pointing out and distinctly claiming the subject matter which the applicant regards as his invention.

Claim 2, 4, 7, 16, are rejected under 35 U.S.C. 112, second paragraph, as being indefinite for failing to particularly point out and distinctly claim the subject matter which applicant regards as the invention.

Claims 2, 4, the step of "detecting a temperature level of the power amplifier" is not clearly recited in the specification. How the "temperature" is detected?

Claim 7 recites the limitation "the integrator" in lines 6, 7. There is insufficient antecedent basis for this limitation in the claim.

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Claim 16, "a second integrator (508)" having the connections as claimed is not clear since "the second control voltage input being coupled to a second control output from the processor" is not shown. As shown in applicant' drawing (fig. 5), amplifier (508) read as a second integrator having its output terminal connected processor (540), its two input terminals are connected to the output of the first integrator (506) and reference voltage source (Vref).

## Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

(e) the invention was described in a patent granted on an application for patent by another filed in the United States before the invention thereof by the applicant for patent, or on an international application by another who has fulfilled the requirements of paragraphs (1), (2), and (4) of section 371(c) of this title before the invention thereof by the applicant for patent.

The changes made to 35 U.S.C. 102(e) by the American Inventors Protection Act of 1999 (AIPA) and the Intellectual Property and High Technology Technical Amendments Act of 2002 do not apply when the reference is a U.S. patent resulting directly or indirectly from an international application filed before November 29, 2000. Therefore, the prior art date of the reference is determined under 35 U.S.C. 102(e) prior to the amendment by the AIPA (pre-AIPA 35 U.S.C. 102(e)).

Claims 1-4, 7, 8, 10-12, 18 are rejected under 35 U.S.C. 102(e) as being anticipated by Miki et al. (6,774,725).

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Regarding claims 1, 3, 7, 8, 10-12, 18, Miki et al. (Fig. 5) disclose a power supply system comprising: detecting the voltage level of a supply source (Vb/400); detecting the voltage level of a supply voltage (Vdd) to the power amplifier provided from the supply source through a pass transistor (231), the pass transistor having a control input (gate); comparing the voltage level of the supply source to a first threshold value; if the voltage level of the supply source is above the first threshold value, comparing the voltage level of the supply voltage to the power amplifier to the voltage level of the supply source; and based on the comparison (amplifier/integrator having inverting/noninverting terminal) of the voltage level of the supply to the power amplifier (210) and the voltage level of the supply source, adjust a voltage level of a control signal provided to the control input (gate) of the pass transistor (231); and a base band and modulation unit (300) can be read as a processor having the function thereof.

Regarding claims 2, 4, wherein the reference circuit is inherently seen having temperature when operated which can be detected accordingly.

#### Allowable Subject Matter

Claims 5, 6, 9, 13-15 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

Claims 16, 17 would be allowable if rewritten or amended to overcome the rejection(s) under 35 U.S.C. 112, 2nd paragraph, set forth in this Office action.

Claims 5, 6, 9 call for, among others, a look up table.

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Claims 13-15 call for, among others, the processor is operative to determine the value of the current being provided to the power amplifier by dividing the difference in the voltage level of the supply power source and the supply voltage level being provided to the power amplifier by the resistance and the step of adjusting the voltage control output is based on the value of the current.

Claims 16, 17 call for, among others, a second integrator.

#### Conclusion

The prior art made of record and not relied upon is considered pertinent to applicant's disclosure. The additional references (Canyon et al. (6,646,511); Dupuis et al. (6,897,730)) show further analogous prior art circuitry.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Khanh V. Nguyen whose telephone number is (571) 272-1767. The examiner can normally be reached from 8:00 AM - 3:30 PM EST.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Robert Pascal can be reached on (571) 272-1769. The fax phone numbers for the organization where this application or proceeding is assigned are (703) 872-9306 for regular communications and (703) 872-9306 for After Final communications.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR.

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Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

KHANH VAN NGUYEN PRIMARY EXAMINER

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